



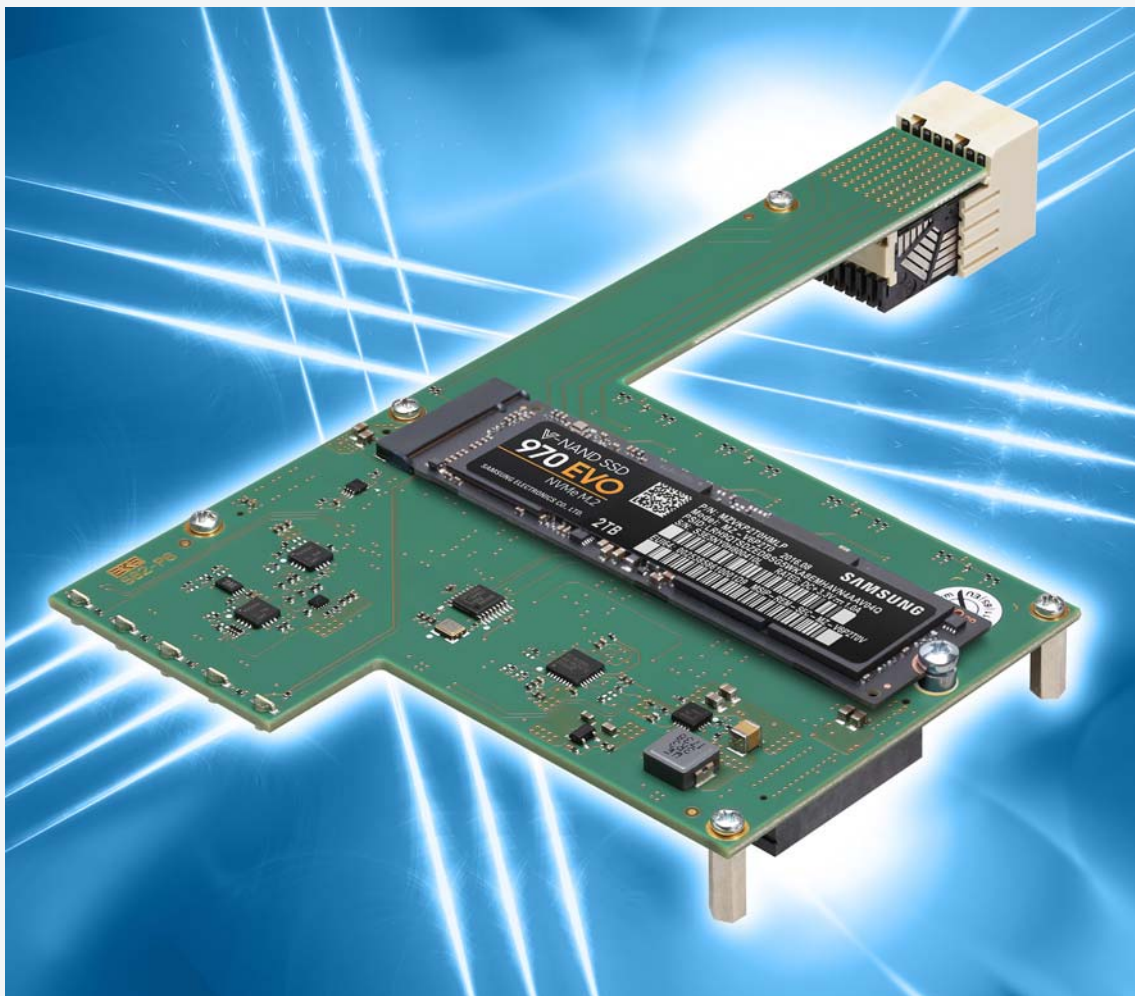
Technical Information

S82-P6

Low Profile Mezzanine Module

M.2 NVMe SSD Storage • Backplane Gigabit Ethernet NICs

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About this Manual

This manual is a short form description of the technical aspects of the S82-P6, required for installation and system integration. It is intended for the advanced user only.

Edition History

Ed.	Contents/ <i>Changes</i>	Author	Date
1	Technical Information S82-P6, English, preliminary edition Text #8827, File: s82_ti.wpd	jj	19 April 2018
2	Added photos	jj	25 July 2018

Related Documents

For a description of CPU cards which may act as carrier board with respect to the S82-P6, please refer to the correspondent CPU user guide, available by download (change URL accordingly for other potential carrier cards).

Download S82-P6 Carrier Card User Guides	
SC4-CONCERTO	www.ekf.com/s/sc4/sc4.html
SC5-FESTIVAL	www.ekf.com/s/sc5/sc5.html

Nomenclature

Signal names used herein with an attached '#' designate active low lines.

Trade Marks

Some terms used herein are property of their respective owners, e.g.

- ▶ Intel, Atom™, Core™, XEON®: ® Intel
- ▶ CompactPCI, CompactPCI PlusIO, CompactPCI Serial: ® PICMG
- ▶ Windows: ® Microsoft
- ▶ EKF, ekf system: ® EKF

EKF does not claim this list to be complete.

Legal Disclaimer - Liability Exclusion

This document has been edited as carefully as possible. We apologize for any potential mistake. Information provided herein is designated exclusively to the proficient user (system integrator, engineer). EKF can accept no responsibility for any damage caused by the use of this manual.

Standards

Reference Documents		
Term	Document	Origin
CompactPCI® Serial	CompactPCI Serial Specification, PICMG® CPCI-S.0	www.picmg.org
Ethernet	IEEE Std 802.1, 802.3 IEEE Std 1588 Precision Time Protocol	standards.ieee.org
M.2	PCI Express M.2 Specification Revision 1.1	www.pcisig.com
NVMe	NVM Express 1.2.1 specification	www.nvmexpress.org

Product Description

Overview

Available as a mezzanine add-on expansion board e.g. to the SC4-CONCERTO and successor CPU carrier cards, the main purpose of the S82-P6 is to provide a Solid State Drive mass storage solution, and in addition to supply the backplane with four Gigabit Ethernet ports.

The S82-P6 is equipped with an M.2 PCIe x4 socket, suitable for a high speed NVMe type SSD module, with a maximum capacity of 2TB as of current, sufficient for installation of an operating system and also data storage.

Four discrete Gigabit Ethernet controllers (aka NIC) are provided on-board, for CompactPCI® Serial backplane communication via the P6 connector according to the 1000BASE-T interface standard.

The Intel® I210-IT Gigabit Ethernet controllers support latest networking features such as Audio Video Bridging (AVB) and Time Sensitive Networking (TSN).

Ethernet is the preferred interface for a multiprocessing configuration. With a suitable Ethernet enabled CompactPCI® Serial backplane, both networking architectures are supported, either single star for reasonable backplane cost, or full mesh for optimum system flexibility.

The S82-P6 connects to the mezzanine expansion connectors HSE1 and HSE2 of the CompactPCI® Serial CPU carrier board, maintaining the 4HP (20.32mm) total assembly height.





Feature Summary

Feature Summary

Form Factor

- ▶ Proprietary size mezzanine module
- ▶ Fits basically into the 4HP (20.3mm) envelope of the CPU carrier board
- ▶ Typically delivered as a ready to use assembly unit (including CPU card)
- ▶ Mounting position right (on top of a CPU board)

Host I/F Connectors

- ▶ High speed mezzanine connectors
- ▶ Suitable for PCI Express® Gen3
- ▶ Bottom mount male connectors HSE1 and HSE2 (high speed expansion)
- ▶ Mating with the carrier card female connectors HSE1/2
- ▶ Board-to-board height 10.8mm for a 4HP assembly

HSE1

- ▶ PCI Express® 1x4 support (dedicated to the NVMe SSD module M.2 socket)
- ▶ Power sourcing 12V/1.5 A maximum continuous current (2 pins)

HSE2

- ▶ PCI Express® 4x1 support (dedicated to the PCIe® based on-board Gigabit Ethernet controllers)
- ▶ Power sourcing 12V/3.0 A maximum continuous current (4 pins)

M.2 NVMe Module Connector

- ▶ Single M.2 socket, maximum M.2 size 2280 (M.2 formerly known as NGFF)
- ▶ Suitable for M.2 NVMe SSD module, key Id M, PCIe x4 I/F
- ▶ PCIe x4 sourced via HSE1 mezzanine connector
- ▶ Maximum (theoretical) 32Gbps I/O data transfer rate (Gen3 PCIe 8GT/s)
- ▶ Power switch, undervoltage lockout, short-circuit protection, quick discharge

Feature Summary

Gigabit Ethernet NICs

- ▶ Four independent Intel® I210-IT PCI Express® Gigabit Ethernet controllers
- ▶ 9.5KB Jumbo Frame support
- ▶ Hardware-based time stamping (IEEE 1588)
- ▶ 802.1AS Timing and Synchronization for Time-Sensitive Applications
- ▶ IEEE 802.1Qav compliant Audio-Video Bridging (AVB)
- ▶ IPv4, IPv6, TCP/UDP checksum offloads
- ▶ Based on four PCI Express® x1 links derived from the mezzanine connector HSE2
- ▶ Driver support for all major operating systems

Applications

- ▶ Low profile mezzanine module for EKF CPU Cards (SC4-CONCERTO and later)
- ▶ 4HP assembly CPU carrier board and S82-P6 mezzanine card
- ▶ Adds SSD mass storage and backplane Ethernet networking to the CPU carrier
- ▶ M.2 based mass storage, 1 x M.2 PCIe x4 socket (NVMe)
- ▶ On-board GbE NICs for backplane communication and multiprocessing via backplane connector P6, four GbE ports (suitable for single star architecture or full mesh or RIO)

Environment & Regulatory

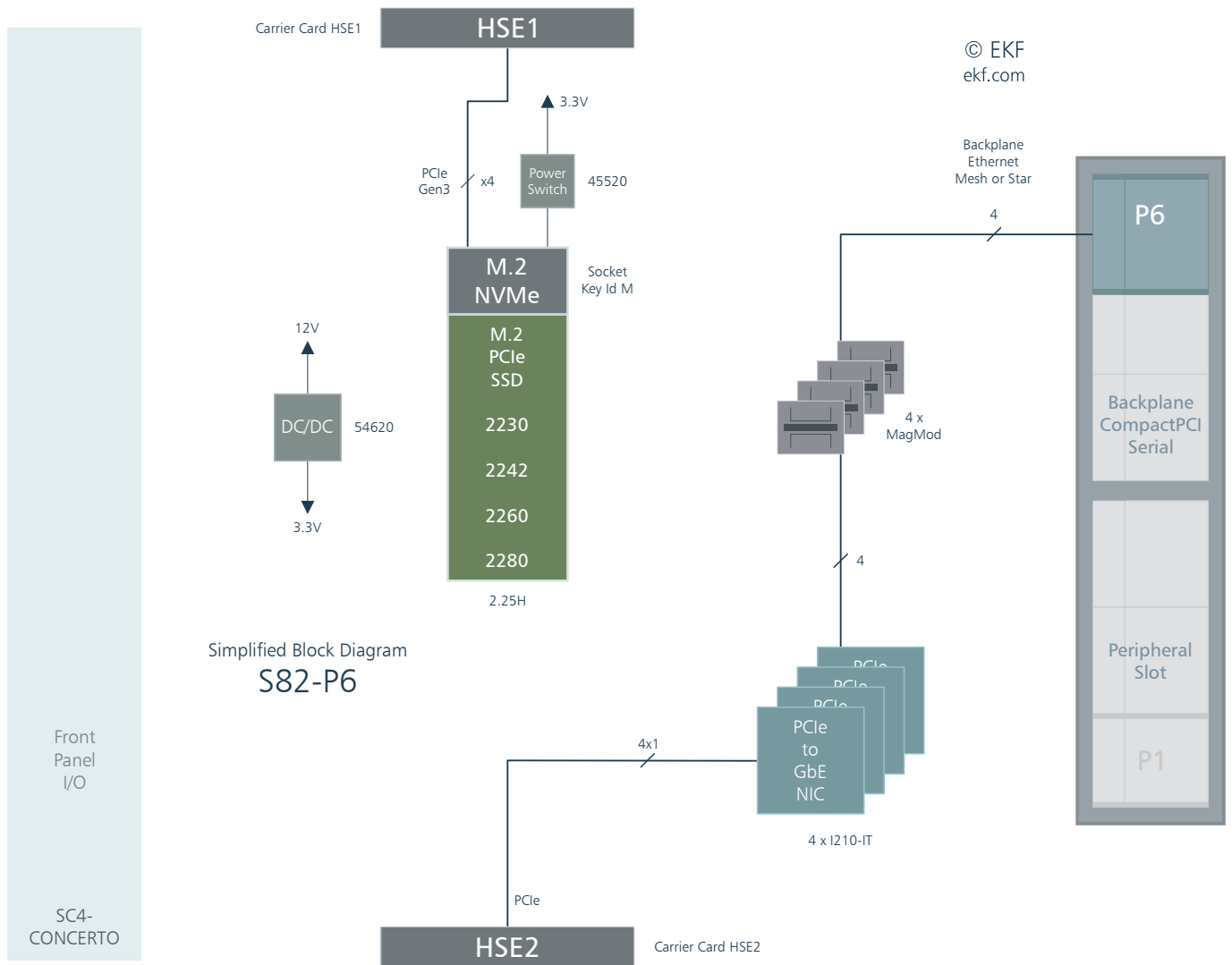
- ▶ Designed & manufactured in Germany
- ▶ Certified quality management according to ISO 9001
- ▶ Long term availability
- ▶ Rugged solution
- ▶ Coating, sealing, underfilling on request
- ▶ RoHS compliant
- ▶ Operating temperature 0°C to +70°C (commercial temperature range) available
- ▶ Operating temperature -40°C to +85°C (industrial temperature range) available
- ▶ Storage temperature -40°C to +85°C, max. gradient 5°C/min
- ▶ Humidity 5% ... 95% RH non condensing
- ▶ Altitude -300m ... +3000m
- ▶ Shock 15g 0.33ms, 6g 6ms
- ▶ Vibration 1g 5-2000Hz
- ▶ MTBF 49.0 years
- ▶ EC Regulations EN55022, EN55024, EN60950-1 (UL60950-1/IEC60950-1)

Custom specific modifications or development on request

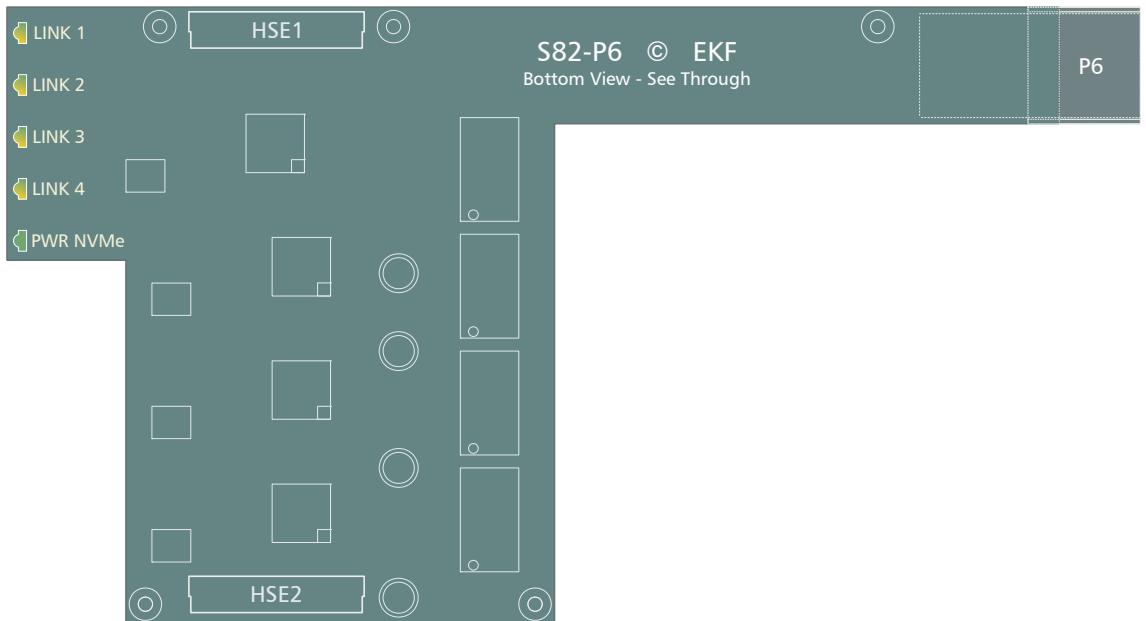
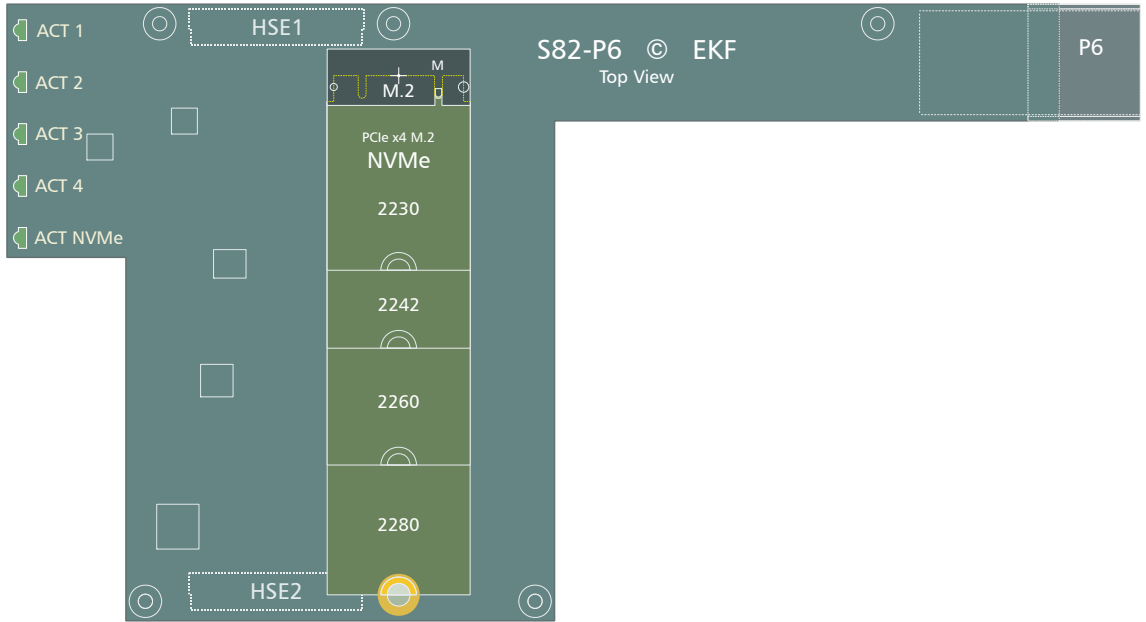
All items are subject to technical changes

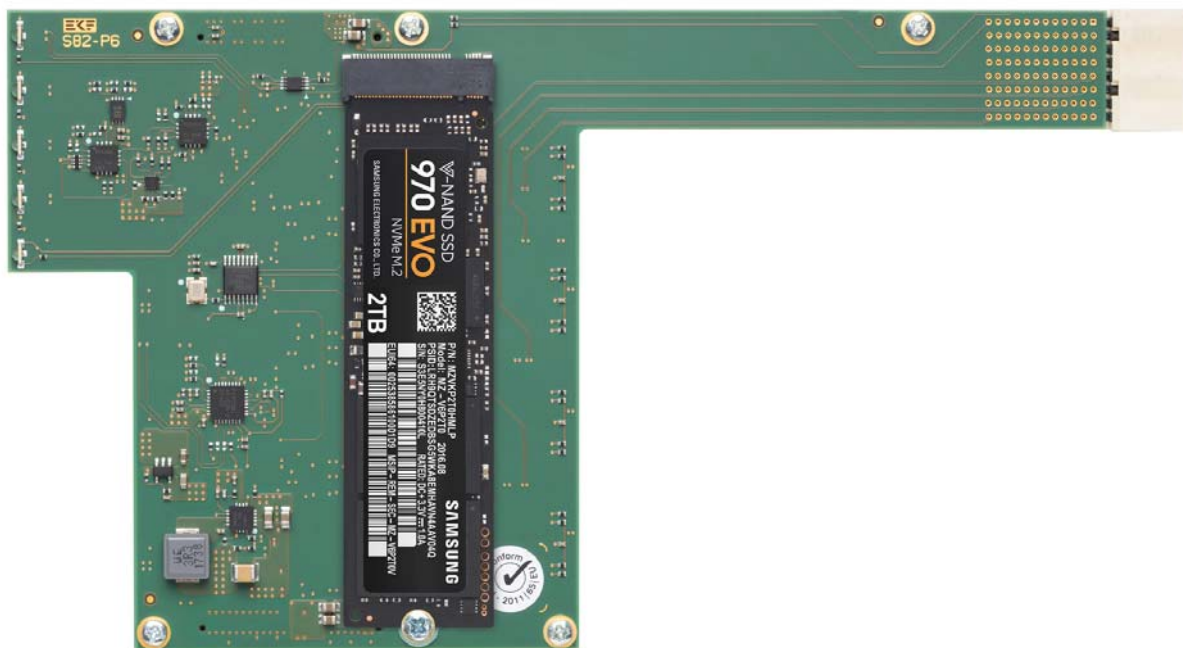
M.2 SSD modules shown in some photos are not scope of delivery

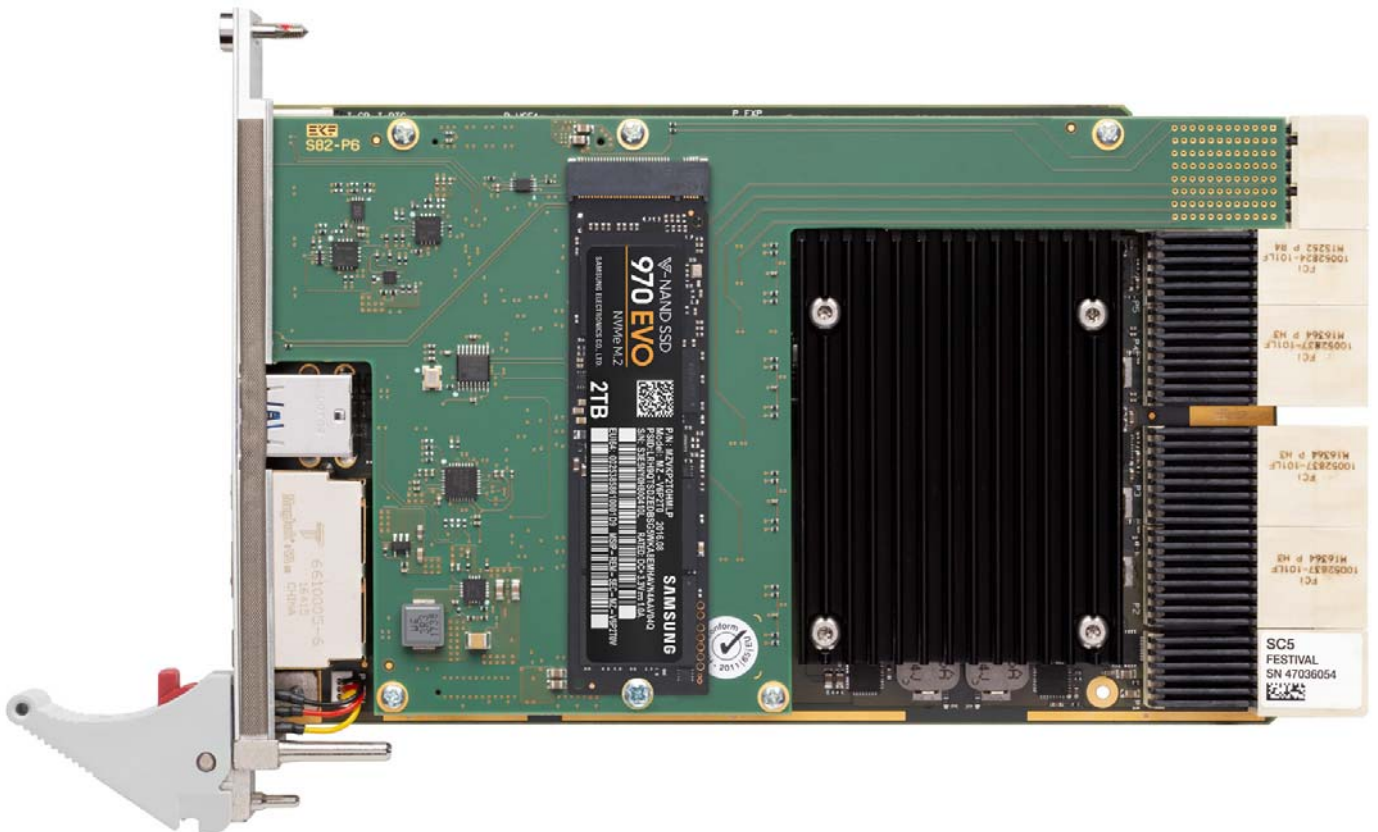
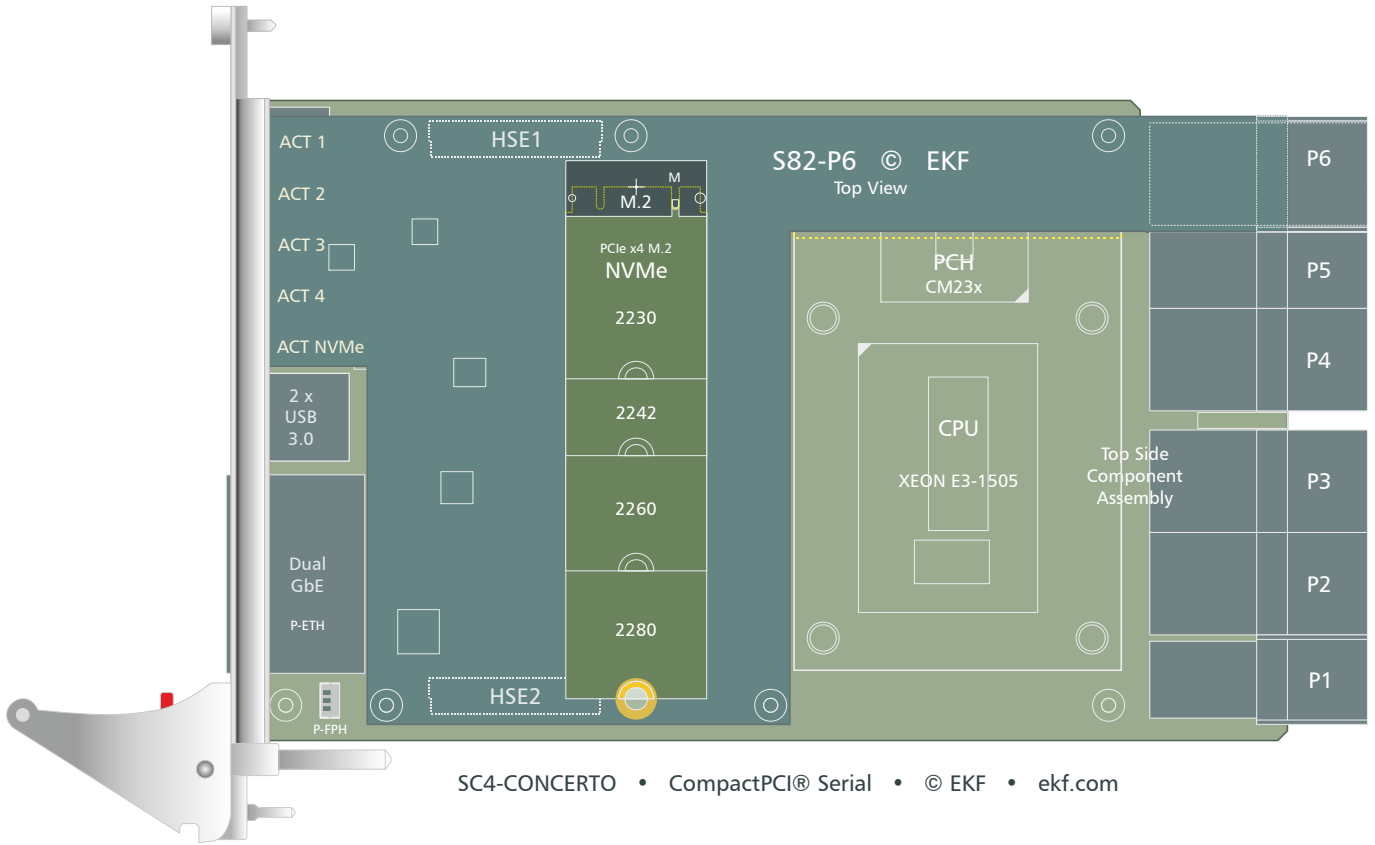
Block Diagram



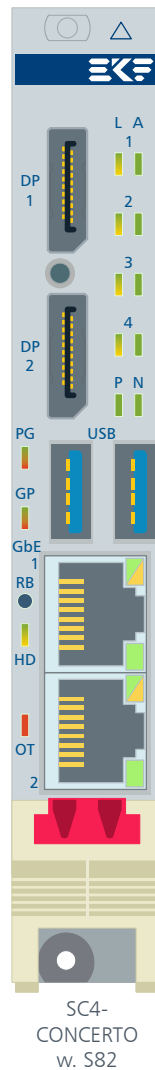
Top/Bottom View Component Assembly







Front Panel



LED L1-L4: Backplane Ethernet ports (P6), green=100Mbps link, orange=1Gbps link

LED A1-A4: Backplane Ethernet ports (P6), blink=Activity

LED P/N: M.2 socket power good, NVMe SSD activity

Backplane Ethernet

The S82-P6 is equipped with four independent Intel® I210-IT Gigabit Ethernet networking controllers. The industrial temperature grade NICs incorporate integrated 1000BASE-T Gigabit Ethernet transceivers, which are wired across magnetics modules to the CompactPCI® Serial backplane connector P6.

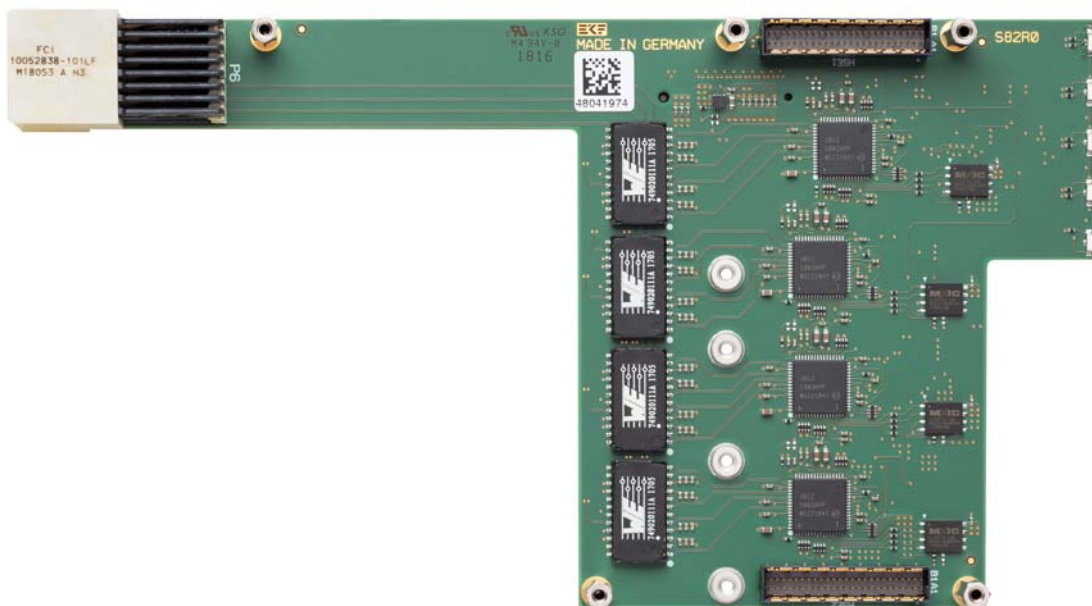
The networking controllers communicate with the CPU carrier card individually via PCI Express® x1 links, passed through the mezzanine interface connector HSE2.

If more than four GbE ports are required for backplane usage, consider the S80-P6 mezzanine module as an alternative solution. The S80-P6 is equipped with an advanced GbE switch, and offers the maximum of eight Gigabit Ethernet channels across the backplane.

A CPU carrier card assembly with the S82-P6 mezzanine typically would be inserted into the system slot of a CompactPCI® Serial backplane, suitable for either star architecture networking, or full mesh backplane Ethernet. Both structures allow backplane multiprocessing and other Ethernet bound communication over P6/J6 between CompactPCI® Serial boards, with higher flexibility (and backplane cost) offered by the mesh version.

An alternative application would be rear I/O Ethernet across P6 (this requires a rear I/O enabled backplane slot).

The I210-IT networking controllers are suitable for Time-Sensitive Applications (TSN). Activity and link status of each backplane Ethernet port can be observed from associated front panel LEDs.

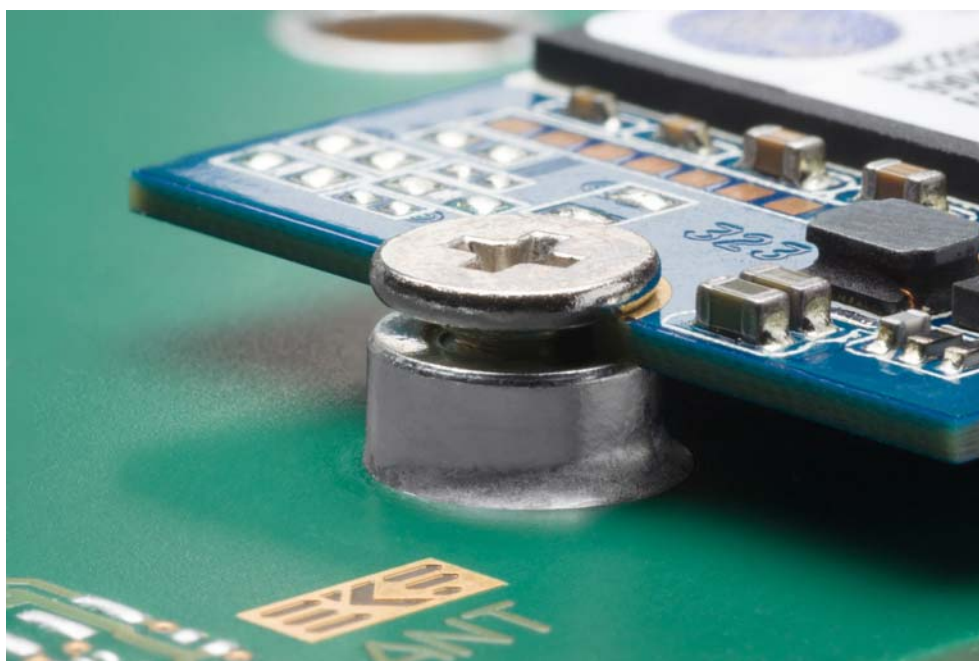


Technical Reference - Connectors

M.2 SSD Host Connector

The S82-P6 is provided with an M.2 (formerly NGFF) module host connector. This socket is M-keyed, for a PCIe x4 based SSD module. After inserted, the M.2 module must be locked manually by a screw (M2.5 threaded inserts provided on the PCB), in order to withstand shock and vibration. The S82-P6 accepts module sizes up to 2280.

The PCIe® x4 NVMe SSD is suitable for demanding applications, but legacy operating systems may not include NVMe protocol drivers. There are also PCIe® x4 based SSDs available for OEMs which comply with the AHCI (SATA) protocol, for legacy systems. When ordering PCIe based SSD modules, be sure to choose the version which is most suitable for your application.



M.2 Module Fixation (Picture Similar)

Please note that the S82-P6 is a carrier card which typically comes without M.2 module(s) populated, unless otherwise expressly ordered. Photos shown within this document and at other places may be equipped with M.2 modules just for application demonstration. If you need a turnkey solution with an M.2 NVMe storage module populated, please contact sales@ekf.com before ordering.

NVMe PCIe x4 M.2 M-Key • Pin 1 - 38 EKF Part #255.50.2.2223.10			
GND	1	2	+3.3V
GND	3	4	+3.3V
PETN3	5	6	NC
PETP3	7	8	NC
GND	9	10	LED1#
PERN3	11	12	+3.3V
PERP3	13	14	+3.3V
GND	15	16	+3.3V
PETN2	17	18	+3.3V
PETP2	19	20	NC
GND	21	22	NC
PERN2	23	24	NC
PERP2	25	26	NC
GND	27	28	NC
PETN1	29	30	NC
PETP1	31	32	NC
GND	33	34	NC
PERN1	35	36	NC
PERP1	37	38	NC



NVMe PCIe x4			
M.2 M-Key continued • Pin 39 - 75			
GND	39	40	SMB_CLK *
PETNO	41	42	SMB_DATA *
PETPO	43	44	ALERT *
GND	45	46	NC
PERNO	47	48	NC
PERPO	49	50	PERST#
GND	51	52	CLKREQ#
REFCLKN	53	54	PEWAKE#
REFCLKP	55	56	RSV
GND	57	58	RSV
M-Key	59	60	M-Key
M-Key	61	62	M-Key
M-Key	63	64	M-Key
M-Key	65	66	M-Key
NC	67	68	SUSCLK
PEDET	69	70	+3.3V
GND	71	72	+3.3V
GND	73	74	+3.3V
GND	75		

* Logic level 1.8V signals - LSF0204 level shifter to 3.3V on-board

PCI Express® M.2 Specification Socket 3 PCIe-based Module Pinout (Module Key M)

Mezzanine Connectors HSE1, HSE2

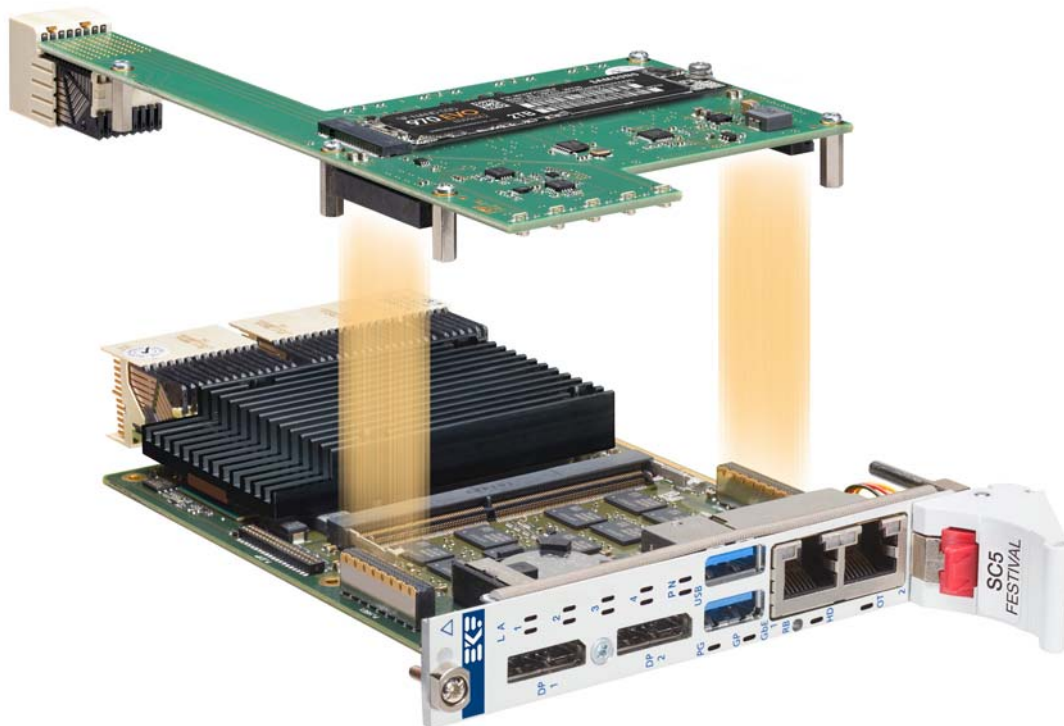
The S82-P6 is provided with two male mezzanine connectors on the bottom side of the PCB, which mate with the female mezzanine connectors on the carrier CPU card, for a resulting board-to-board mounting height of 10.8mm.

HSE1

HSE1 is used to pass a PCIe® x4 link from the CPU carrier card to the on-board M.2 NVMe connector, for a suitable SSD mass storage module.

HSE2

HSE2 is provided to supply the S82-P6 mezzanine with additional PCIe® lanes. A PCIe® x1 link is established for each of the on-board I210-IT Gigabit Ethernet controllers. This requires the CPU carrier card HSE2 connector configured to PCIe® 4x1 (and not PCIe® 1x4 or 2x2).



Carrier card connector 8mm female ERNI Microspeed 275.90.08.068.01

Supplement 1mm male connector for nominal height 9mm (C4*, B2B 9.5mm)

Supplement 2mm male connector for nominal height 10mm (S2*, S4*, B2B 10.0mm)

Supplement 2mm male connector for nominal height 10mm (S6*, S8*, B2B 10.8mm)

Supplement 10mm male connector for nominal height 18mm (SC* side card, B2B 18.7mm)

High Speed Expansion P-HSE1				
<p>© EKF 275.90.01.068.51 ekf.com</p> <p>1.00mm Pitch High Speed Male Connector</p>	CFG_34 *	b1	a1	CFG_12 *
	3_PCIE_TXP	b2	a2	1_PCIE_TXP
	3_PCIE_TXN	b3	a3	1_PCIE_TXN
	GND	b4	a4	GND
	3_PCIE_RXN	b5	a5	1_PCIE_RXN
	3_PCIE_RXP	b6	a6	1_PCIE_RXP
	GND	b7	a7	GND
	4_PCIE_TXP	b8	a8	2_PCIE_TXP
	4_PCIE_TXN	b9	a9	2_PCIE_TXN
	GND	b10	a10	GND
	4_PCIE_RXN	b11	a11	2_PCIE_RXN
	4_PCIE_RXP	b12	a12	2_PCIE_RXP
	GND	b13	a13	GND
	2_USB3_TXP	b14	a14	1_USB2_P
	2_USB3_TXN	b15	a15	1_USB2_N
	GND	b16	a16	GND
	2_USB3_RXP	b17	a17	2_USB2_P
	2_USB3_RXN	b18	a18	2_USB2_N
	GND	b19	a19	GND
	PCIE_CLK_P	b20	a20	1_2_USB_OC#
	PCIE_CLK_N	b21	a21	PLTRST#
	+5VS ¹⁾	b22	a22	+3.3VS ¹⁾
	+5VS ¹⁾	b23	a23	+3.3VS ¹⁾
	+5VPS ²⁾	b24	a24	+3.3VA ³⁾
	+12VPS ²⁾	b25	a25	+12VPS ²⁾

* CFG_12 and CFG_34 = open (10k PU on CPU carrier board) indicating that a PCIe x4 link is requested

- 1) Power rail switched on in S0 state only
- 2) Power rail switched on in S0-S4 state
- 3) Power always on

Carrier card connector 8mm female ERNI Microspeed 275.90.08.068.01

Supplement 1mm male connector for nominal height 9mm (C4*, B2B 9.5mm)

Supplement 2mm male connector for nominal height 10mm (S2*, S4*, B2B 10.0mm)

Supplement 2mm male connector for nominal height 10mm (S6*, S8*, B2B 10.8mm)

Supplement 10mm male connector for nominal height 18mm (SC* side card, B2B 18.7mm)

High Speed Expansion P-HSE2				
<p>© EKF 275.90.01.068.51 ekf.com</p> <p>1.00mm Pitch High Speed Male Connector</p>	3_PCIE_TXP	b1	a1	1_PCIE_TXP
	3_PCIE_TXN	b2	a2	1_PCIE_TXN
	GND	b3	a3	GND
	3_PCIE_RXN	b4	a4	1_PCIE_RXN
	3_PCIE_RXP	b5	a5	1_PCIE_RXP
	GND	b6	a6	GND
	4_PCIE_TXP	b7	a7	<i>2_PCIE_TXP</i>
	4_PCIE_TXN	b8	a8	<i>2_PCIE_TXN</i>
	GND	b9	a9	GND
	4_PCIE_RXN	b10	a10	2_PCIE_RXN
	4_PCIE_RXP	b11	a11	2_PCIE_RXP
	GND	b12	a12	GND
	<i>DP_LANE2_P</i>	b13	a13	<i>DP_LANE0_P</i>
	<i>DP_LANE2_N</i>	b14	a14	<i>DP_LANE0_N</i>
	GND	b15	a15	GND
	<i>DP_LANE3_P</i>	b16	a16	<i>DP_LANE1_P</i>
	<i>DP_LANE3_N</i>	b17	a17	<i>DP_LANE1_N</i>
	GND	b18	a18	GND
	<i>DP_AUX_P</i>	b19	a19	PCIE_CLK_P
	<i>DP_AUX_N</i>	b20	a20	PCIE_CLK_N
	<i>DP_CFG1</i>	b21	a21	GND
	<i>DP_HPD</i>	b22	a22	SMB_SCL ¹⁾
	PLTRST#	b23	a23	SMB_SDA ¹⁾
	+12VPS ²⁾	b24	a24	+12VPS ²⁾
	+12VPS ²⁾	b25	a25	+12VPS ²⁾

italic/grey pins are NC (shown for reference only)

PCIe® can pre-configured 1x4, 2x2, 4x1 via soft-straps (Flash image CPU carrier card). For S82-P6 PCIe® 4x1 is mandatory. If misaligned, only one or two NICs will be present after system enumeration.

1) Connection to SMBus, isolated after system reset 2) Power rail switched on in S0-S4 state

Backplane Connector P6

P6 CompactPCI® Serial Peripheral Slot Backplane Connector												
EKF Part #250.3.1208.20.02 • 96 pos. 12x8, 18mm width												
P6	A	B	C	D	E	F	G	H	I	J	K	L
8	PU 1)	<i>8</i> <i>ETH</i> <i>A+</i>	<i>8</i> <i>ETH</i> <i>A-</i>	PU 2)	<i>8</i> <i>ETH</i> <i>B+</i>	<i>8</i> <i>ETH</i> <i>B-</i>	GND	<i>8</i> <i>ETH</i> <i>C+</i>	<i>8</i> <i>ETH</i> <i>C-</i>	GND	<i>8</i> <i>ETH</i> <i>D+</i>	<i>8</i> <i>ETH</i> <i>D-</i>
7	<i>7</i> <i>ETH</i> <i>A+</i>	<i>7</i> <i>ETH</i> <i>A-</i>	GND	<i>7</i> <i>ETH</i> <i>B+</i>	<i>7</i> <i>ETH</i> <i>B-</i>	GND	<i>7</i> <i>ETH</i> <i>C+</i>	<i>7</i> <i>ETH</i> <i>C-</i>	GND	<i>7</i> <i>ETH</i> <i>D+</i>	<i>7</i> <i>ETH</i> <i>D-</i>	GND
6	GND	<i>6</i> <i>ETH</i> <i>A+</i>	<i>6</i> <i>ETH</i> <i>A-</i>	GND	<i>6</i> <i>ETH</i> <i>B+</i>	<i>6</i> <i>ETH</i> <i>B-</i>	GND	<i>6</i> <i>ETH</i> <i>C+</i>	<i>6</i> <i>ETH</i> <i>C-</i>	GND	<i>6</i> <i>ETH</i> <i>D+</i>	<i>6</i> <i>ETH</i> <i>D-</i>
5	<i>5</i> <i>ETH</i> <i>A+</i>	<i>5</i> <i>ETH</i> <i>A-</i>	GND	<i>5</i> <i>ETH</i> <i>B+</i>	<i>5</i> <i>ETH</i> <i>B-</i>	GND	<i>5</i> <i>ETH</i> <i>C+</i>	<i>5</i> <i>ETH</i> <i>C-</i>	GND	<i>5</i> <i>ETH</i> <i>D+</i>	<i>5</i> <i>ETH</i> <i>D-</i>	GND
4	GND	<i>4</i> <i>ETH</i> <i>A+</i>	<i>4</i> <i>ETH</i> <i>A-</i>	GND	<i>4</i> <i>ETH</i> <i>B+</i>	<i>4</i> <i>ETH</i> <i>B-</i>	GND	<i>4</i> <i>ETH</i> <i>C+</i>	<i>4</i> <i>ETH</i> <i>C-</i>	GND	<i>4</i> <i>ETH</i> <i>D+</i>	<i>4</i> <i>ETH</i> <i>D-</i>
3	<i>3</i> <i>ETH</i> <i>A+</i>	<i>3</i> <i>ETH</i> <i>A-</i>	GND	<i>3</i> <i>ETH</i> <i>B+</i>	<i>3</i> <i>ETH</i> <i>B-</i>	GND	<i>3</i> <i>ETH</i> <i>C+</i>	<i>3</i> <i>ETH</i> <i>C-</i>	GND	<i>3</i> <i>ETH</i> <i>D+</i>	<i>3</i> <i>ETH</i> <i>D-</i>	GND
2	GND	<i>2</i> <i>ETH</i> <i>A+</i>	<i>2</i> <i>ETH</i> <i>A-</i>	GND	<i>2</i> <i>ETH</i> <i>B+</i>	<i>2</i> <i>ETH</i> <i>B-</i>	GND	<i>2</i> <i>ETH</i> <i>C+</i>	<i>2</i> <i>ETH</i> <i>C-</i>	GND	<i>2</i> <i>ETH</i> <i>D+</i>	<i>2</i> <i>ETH</i> <i>D-</i>
1	<i>1</i> <i>ETH</i> <i>A+</i>	<i>1</i> <i>ETH</i> <i>A-</i>	GND	<i>1</i> <i>ETH</i> <i>B+</i>	<i>1</i> <i>ETH</i> <i>B-</i>	GND	<i>1</i> <i>ETH</i> <i>C+</i>	<i>1</i> <i>ETH</i> <i>C-</i>	GND	<i>1</i> <i>ETH</i> <i>D+</i>	<i>1</i> <i>ETH</i> <i>D-</i>	GND

pin assignments reflect standard mount P6 connector
italic/grey pins are NC (shown for reference only)

- 1) DECT_RIO Signal
- 2) DECT_BPR Signal

On backplanes suitable for Ethernet (either star or mesh) both pins DECT_* are tied to GND. Open pins indicate a P6 rear I/O enabled backplane slot. The CPU carrier card with S82-P6 mezzanine must not be inserted into backplane slots which are designated for rear I/O over P6/J6 with an other signal assignment than Ethernet. A RIO module with up to 8 x RJ45 GbE ports is planned by EKF, as an alternate to backplane networking, suitable for an P6/J6 rear I/O enabled backplane slot.

Ordering Information

Ordering Information
For popular S82-P6 SKUs please refer to www.ekf.com/liste/liste_21.html#S82

Please note that the S82-P6 is a mezzanine board which typically comes without an M.2 module populated, unless otherwise expressly ordered. Photos shown within this document and at other places may be equipped with M.2 modules just for application demonstration. If you need a turnkey solution with an M.2 NVMe SSD storage module populated, please contact sales@ekf.com before ordering.

Alternate Products

Low Profile CPU Card Mezzanine Storage Modules	
S20-NVME	M.2 NVMe Socket, 1 x Type-C USB Front I/O
S40-NVME	M.2 NVMe & M.2 SATA Sockets, 2 x Type-C Front I/O
S80-P6	M.2 NVMe & GbE Switch for 8-Port Backplane Ethernet

High Performance Embedded

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